

Image

Image/2811

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
YOR920000588US2 (16966)

In Re Application Of: Tak H. Ning

MAR 29 2004

Serial No.
09/757,965

Filing Date
January 10, 2001

Confirmation No.
9386

Examiner
Thien F. Tran

Art Unit
2811

Invention: FULLY DEPLETED COLLECTOR SILICON ON INSULATOR (SOI) BIPOLAR TRANSISTOR USEFUL ALONE OR IN SOI BiCMOS

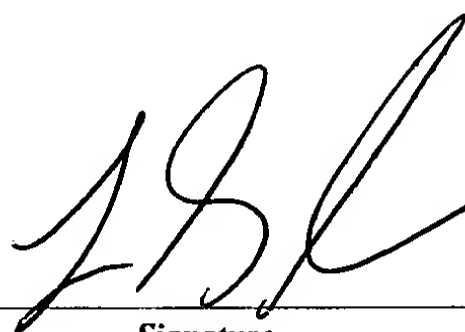
Address to:

Commissioner for Patents
P.O. Box 1450
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Transmitted herewith are:

14 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).



Signature

Dated: March 24, 2004

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I certify that this document and attached formal drawings are being deposited on March 24, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Leslie S. Szivos, Ph.D.

Typed or Printed Name of Person Mailing Correspondence